

D

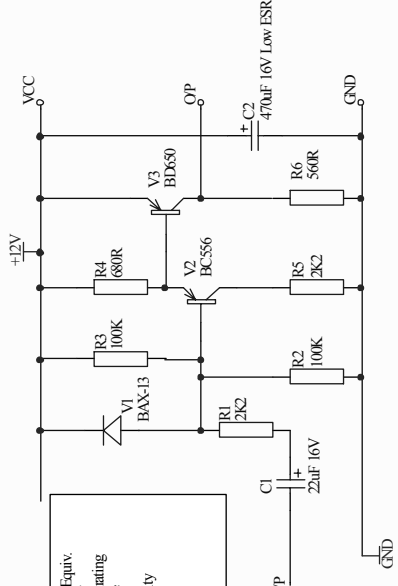
C

B

A

Instructions for Uniden 510  
 Find and remove diode D7 - top left corner of large transformer - T1  
 Connect the driver circuit output to + end of D7.  
 Find and remove C7 - 0.47uF Electro - next to D7.  
 Find and remove diode D14.  
 Remove CS1 - 470uF 25V adjacent to lower-right T1.  
 Drill a 2mm hole adjacent to pads for power wires to pass through.  
 Disable the audio input from the mic by:  
 Lift one end of R89  
 Lift one end of R93  
 Connect the driver circuit input to the junction of R89 & R93.

Circuit Description.  
 V1 used to fix the bias of the input capacitor with signal.  
 V2 is normally on with no signal.  
 V3 is biased on by V2 therefore the output is at +VCC and the modulation is maximum  
 This assists with the starting of the tube and tuning of the antenna matching.  
 When the input goes to "1" then V2 turns off  
 removing the drive from V3. V3 output goes to GND.  
 With Serrona Freq Gen at 10kHz rise and fall times are  $\leq$  1uS.



V1 - generic signal diode - IN4148 or Equiv.  
 V2 - generic PNP TO92 AF transistor  
 V3 - generic PNP AF power transistor rating  
 >=3A, 40V, 20W TO-220 package  
 R1 may be adjusted to change sensitivity

Connect to Freq Gen  
 or PC sound card

Title		Uniden 510/520 Modifications for digital input	
Size	Number	Revision	
A4			
Date	24-Sep-2003	Sheet of	
File	D:\rev\uniden 510\Digital Drivesch	Drawn By:	